

Amendments to the Specification:

Please replace the Title with the following rewritten Title:

COMMUNICATION NETWORK WITH ARRANGEMENT FOR REPLACING A
MALFUNCTIONING ACTIVE MASTER WITH A NEW ACTIVE MASTER

Please replace the Abstract of the invention with the new Abstract attached on a separate sheet at the end of this Amendment.

Please replace the paragraph beginning at page 4, line 6 with the following rewritten paragraph.

In FIG. 1, RC is a remote control functioning as a control unit in this embodiment. D1-D5 are devices. Each of the devices is equipped with a master M, a communication interface CI, a control circuit CC, a device operating circuit DOC that in this embodiment is formed by a ballast circuit and a lamp LA connected to the ballast circuit. Each master is equipped with a transceiver for wireless communication between the control unit and the master and between the master and the communication interfaces of the devices. Each master is also equipped with a beacon means for transmitting periodical signals when its active and with detecting means for detecting the periodical signals transmitted by an active master. The detecting means comprise a timer circuit for timing the time lapse during which the periodical signal is absent. Each of the masters further comprises means for activating itself in case the active master fails. Each master M or part of each master M may be realized by means of a microprocessor and a memory comprising software. In a concrete embodiment of the ~~communicatio~~ communication network shown in FIG. 1, the communication interface CI may comprise an ~~antenna~~ antenna, a

~~radiochip~~ radio chip such as for instance the CHIPCON EM2430, a demodulator and a modulator etc. The control circuit CC will generally comprise a decoder for decoding the signals from the communication interface CI and an interpreter supplying the proper signals to the device operating circuit DOC. The control circuit CC and the device operating circuit DOC may be realized by means of the same microprocessor as is comprised in the master M together with the proper software. The communication interface CI can communicate with the control circuit CC by means of for instance I2C, SPI or UART or any standard communication interface. The microprocessor can be an 8-bits type microprocessor, for instance the Philips 8051. The communication between the control circuit CC and the device operating circuit DOC can be by means of GPIO.